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GROUP
Unassigned

[illegible]

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
N I	EPO133004	02/85	EP				

[illegible]

Nadeem Gh

11-27-03

{03/01}

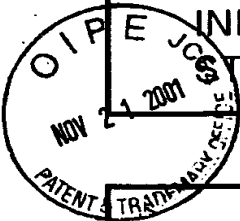
APPLICATION No.
09/807,077

FILING DATE
April 9, 2001

GROUP
2184

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

#5



U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no
NJ	EP 0813152		EPO	Dec 17, 1997		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
N1	Furht, B., et al., "A Survey of Microprocessor Architectures for Memory Management," vol 20, no. 3, 19 pages		
Examiner Signature	Nadeem Tahir		Date Considered 11-27-03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.